Docket No.: 056937-0089

#### **PATENT**

### TES PATENT AND TRADEMARK OFFICE

In re Application of

Response Under 37 CFR 1.116 - Expedited Procedure

Customer Number: 20277

Kenichiro UDA

Confirmation Number: 3242

Application No.: 10/658,195

Group Art Unit: 2825

Filed: September 10, 2003

Examiner: Yelena Rossoshek

For: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF INTEGRATED CIRCUIT

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	13	20	0	\$50.00 =	\$0.00
Independent Claims	1	3	0	\$200.00 =	\$0.00
		Multiple dependent claims newly presented			\$0.00
		Fee for extension of time			\$0.00
					\$0.00
		Total of Above Calculations			\$0.00

П Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WIL & EMERY LLP

Michael E. Fogart Registration No 6,139

Please recognize our Customer No. 20277 as our

correspondence address.

600 13th Street, N.W.

 $\boxtimes$ 

Washington, DC 20005-3096 Phone: 202.756.8000 MEF:kap

Facsimile: 202.756.8087 Date: December 19, 2006



Docket No.: 056937-0089

# **PATENT**

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kenichiro UDA RESPONSE UNDER 37 CFR 1.116

**EXPEDITED PROCEDURE** 

Application No.: 10/658,195

Customer No.: 20277

Filed: September 10, 2003

Confirmation No.: 3242

Group Art Unit: 2825

Examiner: Yelena Rossoshek

Title: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF

INTEGRATED CIRCUIT

# **AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated September 20, 2006, having a three-month shortened statutory period for response set to expire December 20, 2006, please amend the above-identified application as follows.